

FiE on Firmware

Finding Vulnerabilities in Embedded Systems using Symbolic Execution

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FiE in a Nutshell



- Symbolic execution tailored to embedded **firmware**
 - Detects common firmware **vulnerabilities**
 - Deals with **domain-specific challenges**
 - Able to **verify** small programs
- Tested on 99 programs
 - Found 22 **bugs**
 - **Verified** memory safety for 52 programs

Example Attack: WOOT 2012



[Frisby et al., 2012]

Embedded Systems: Lots of Attacks

designlines INTERNET OF THINGS

Design How-To
Embedde
attacks

Peter Clarke
2/26/2013

**Source code analysis is
helpful on desktop**

Could be transitioned to
firmware

Siemens Simatic S7-1200
could impersonate the
PROFINET-FU over ISO-

Kell

1. Rem

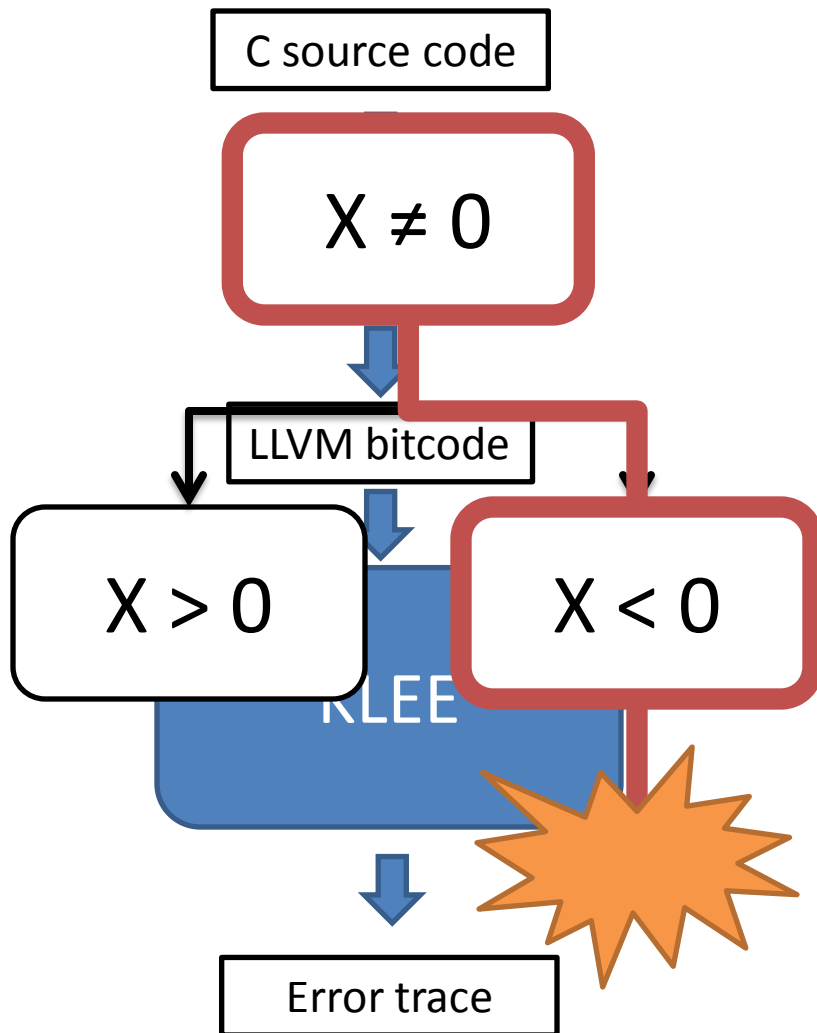
VN

Presented By:
Nils
Rafael Dominguez Vega

July 25

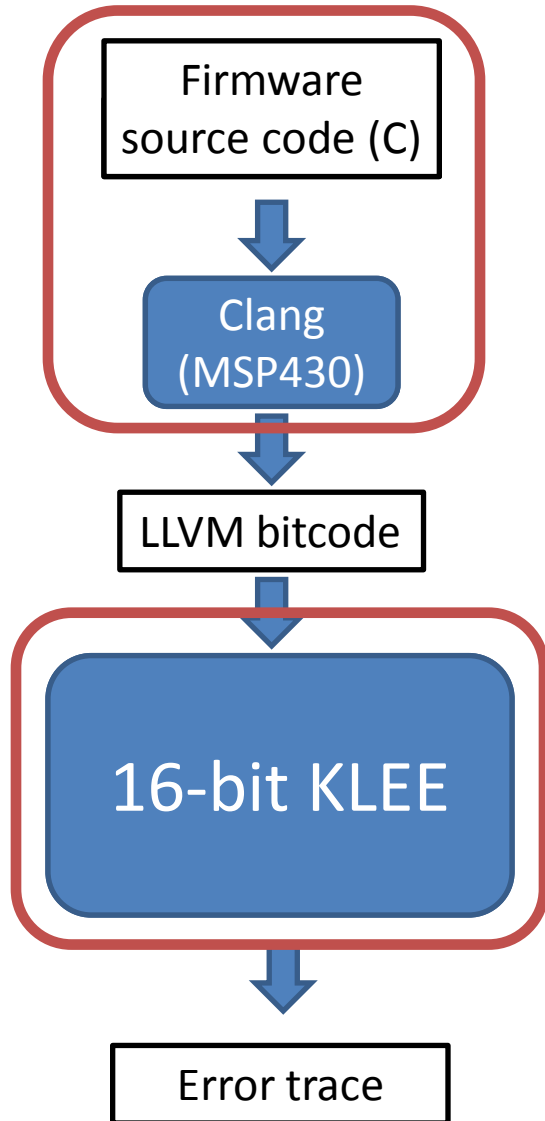
... Little Work on Detecting Vulnerabilities

Symbolic Execution



- Represents program input as sets of constraints
- Explores multiple feasible paths for bugs
- Provide detailed trace to vulnerability
- KLEE
 - Popular, mature tool
 - Average > 90% line coverage
 - Finds memory safety violations

KLEE: Performance on MSP430



- Why MSP430?
 - Popular, widely deployed
 - Security applications
 - Has clang support
- KLEE ported to 16-bit
- Evaluated 99 programs
 - 12 TI Community
 - 78 Github
 - 8 USB protocol stack
 - 1 Synthetic (cardreader)
- Average instruction coverage for MSP430 < 6%
 - Most programs < 1%

Challenges of MSP430 Code

- Peripheral access with I/O Ports
- Environment interaction via implicit memory mapping

Challenge #1
Architecture
Diversity

> 400 variants of MSP430

```
while (true){  
    if (*P0IN)  
        len = *P0IN;  
    BIS_SR(GIE);  
    if (!*P0IN)  
        strncpy(dst, src, len);  
}
```

PORT 2 ISR

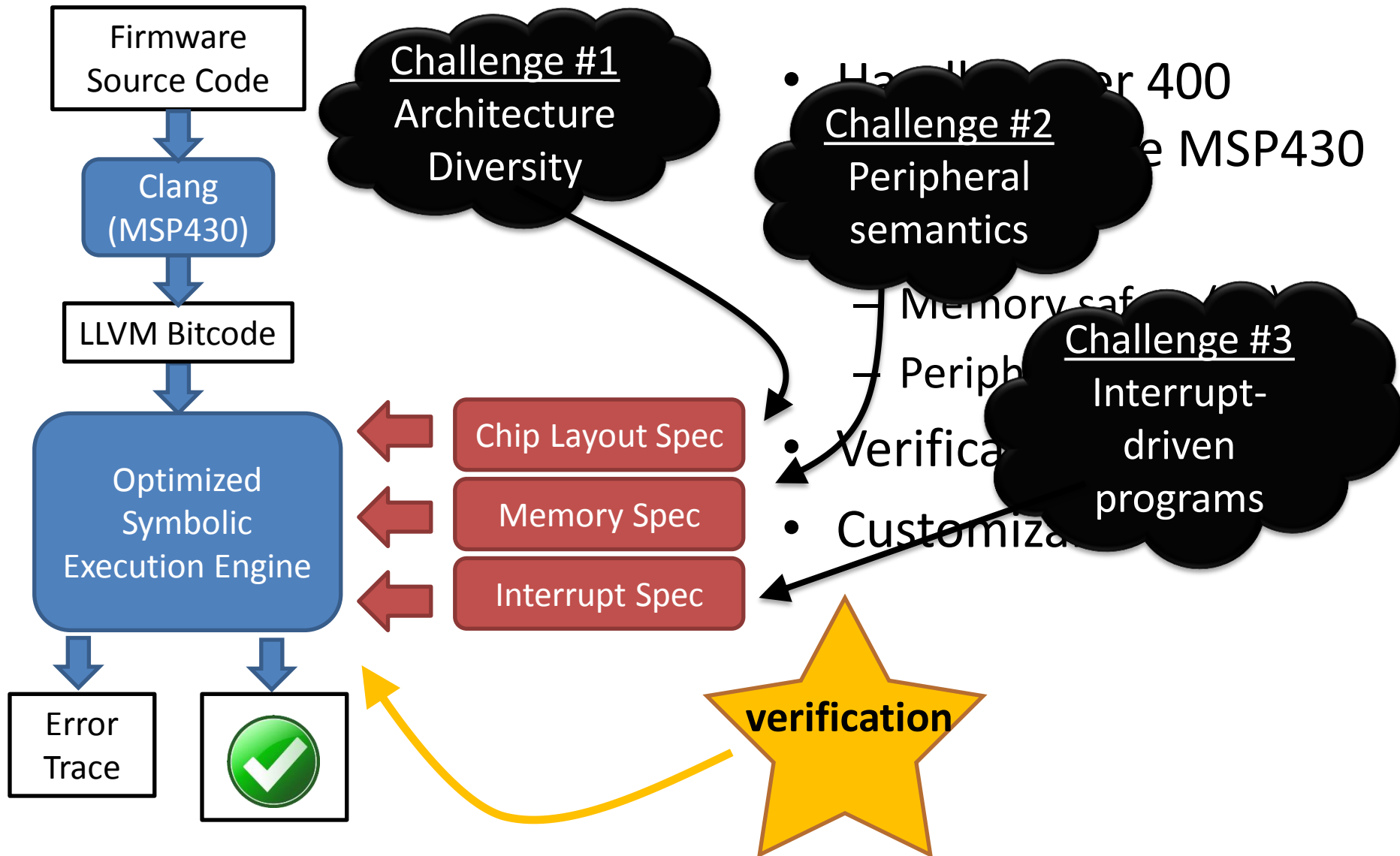
P0IN = 0x0;

Challenge #2
Peripheral
semantics

value!



FiE on Firmware



FiE on Memory

Assume adversary controls peripherals
Allow users to supply custom libraries

Chip Layout Table

```
addr P1IN 0x20 1
```

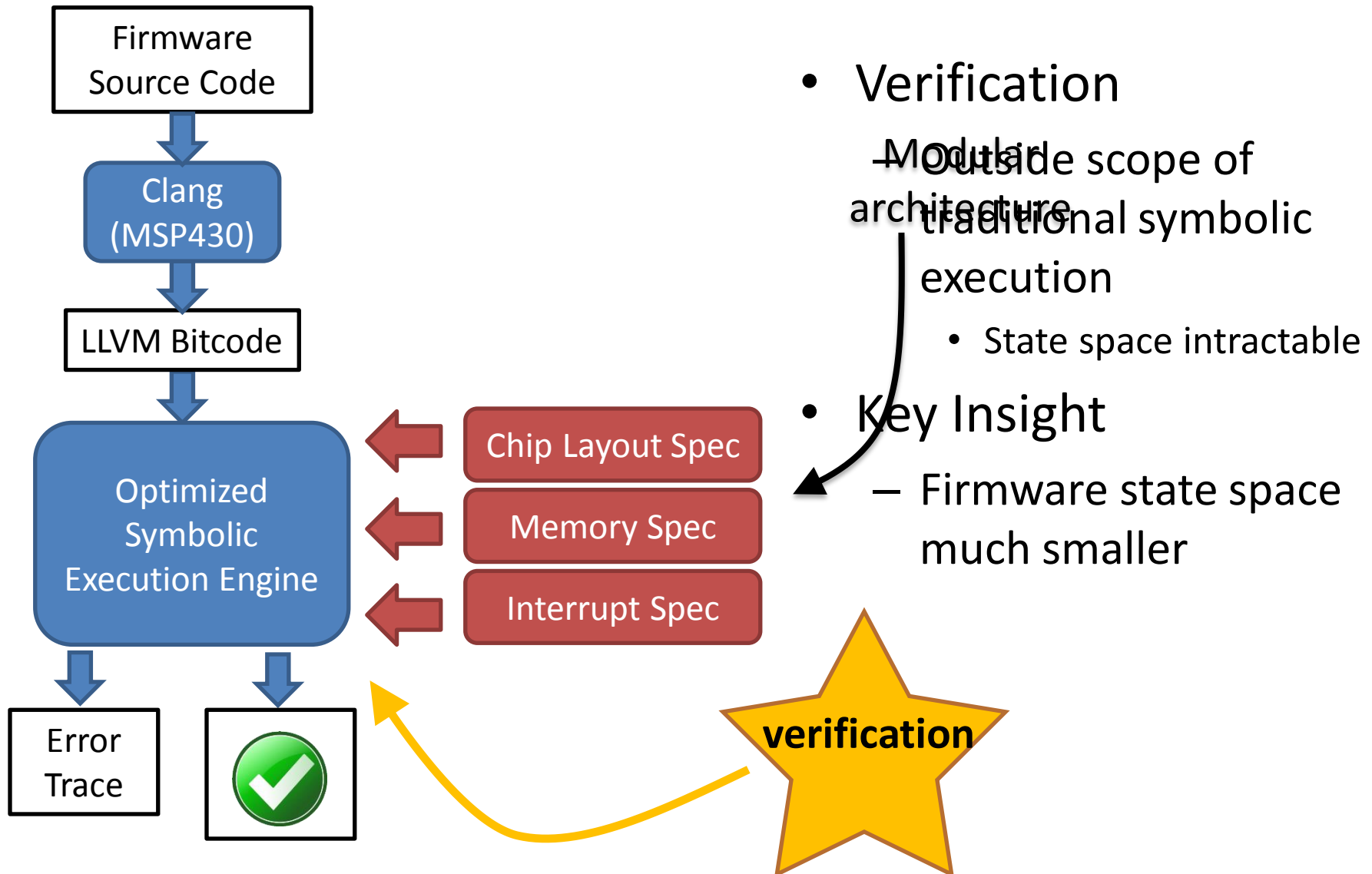
Memory Library

```
P1IN_READ:  
fresh_symbolic()
```

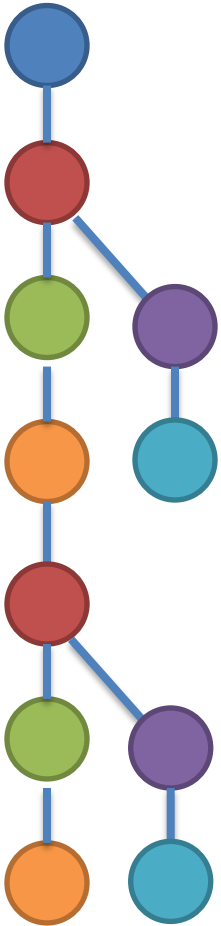
```
while (true){  
  if (*0x20) ←  $\partial_1$   
    len = *0x20; ←  $\partial_2$   
    _BIS_SR(GIE);  
    if (!*0x20) ←  $\partial_3$   
      strncpy(dst, src, len);  
}
```

PORT_2_ISR
*0x22 = 0x0;

Challenges and Opportunities



FiE on Verification



Infinite program paths

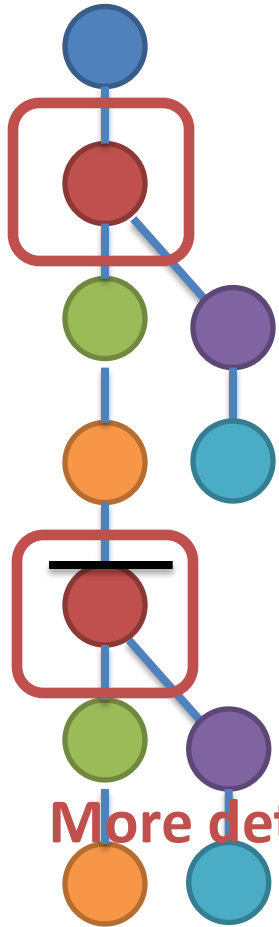
Analysis stuck executing already-seen states

Prevents verification

```
while (true) {  
    if (*0x20)  
        len = *0x20;  
    _BIS_SR(GIE);  
    if (!*0x20)  
        strncpy(dst, src, len);  
}
```

```
PORT_2_ISR  
*0x22 = 0x0;
```

FiE on Verification



- Log all execution states
- Pruning
 - Detect redundant states and terminate them
 - Redundant states; redundant successors
- Smudging
 - replace frequently-changing concrete memory with symbolic
 - Complete
 - May have FPs

FiE on Firmware

Chip Layout Spec

Memory Spec

Interrupt Spec

Challenge #1
Architecture
Diversity

Challenge #2
Peripheral
semantics

Challenge #3
Interrupt-
driven
programs

Optimized
Symbolic
Execution Engine

verification

Evaluation



Corpus:

12 TI Community
1 Synthetic (cardreader)
8 USB protocol stack
78 Github

- Amazon EC2
 - Automated tests (scripts available)
 - 50 minute runs
- Test Versions:
 - 16-bit KLEE
 - baseline
 - FiE
 - Symbolic + plugin
 - FiE + pruning
 - FiE + pruning + smudging

Bugfinding Results



- 22 bugs across the corpus (smudge)
 - Verified manually
 - 21 found in the MSP430 USB protocol stack
 - 1 misuse of flash memory
- Emailed developers

Coverage Results

Mode	Average % Coverage	False Positives	Verified
Baseline	5.9	92	0
Symbolic	71.1	0	7
Prune	74.4	0	35
Smudge	79.4	1	53

Thanks!

Summary

Initiated work for MSP430 automated bugfinding

Modular, conservative symbolic execution

Supported verification and bugfinding

Download FiE

www.cs.wisc.edu/~davidson/fie