

Taming Latency in Software Defined Networks

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LATENCY IN SDN

Timely interaction between an SDN controller and switches is crucial to many applications like MicroTE, Fast Failover, Mobility, etc. These applications assume that the latency in interacting with the network switches is constant and negligible. However our measurement studies shows that this latency is significant. Moreover, it varies with the switch platforms, type of operations performed, table occupancy and concurrent operations on the switches.

Using grey-box probing, we narrow down the key factors for these latencies to be TCAM Organization, Low power switch CPU and software implementation inefficiencies. To overcome the latencies and achieve responsive control, we develop a systematic framework leveraging both the logically central view and global control in SDN, and the dissection of latencies from our measurement study.



РНҮ	PHY		
		Switch	

O4: Rule is updated in table

INBOUND LATENCY

- Increases with flow arrival rate
- Increases with interference from outbound msgs

Flow Arrival Rate (packets/sec)	Mean Delay per packet_in (msec)
100	3.32
200	8.33



OUTBOUND LATENCY - INSERTION

- Affected by priority insertion patterns
- Affected by the table occupancy





(b) Burst size 100, incr. priority

burst size

low priority rules

table 100 — table 400





OUTBOUND LATENCY – MODIFY/DELETE

- Higher than Insertion latency
- Not affected by rule priority but affected by table occupancy









Goal: Select paths across the network such that \bullet *installation delay is minimized* and *the network* objective is satisfied

Minimizes the aggregate impact of both rule **displacement** in TCAM and **total number** of rules

Rule Offloading

- Networks with tunnels typically sees less churn *in forwarding state* in underlay network as compare to the end points
- Leverages this characteristic to *offload rules*
- Goal: Minimizes the installation latency by offloading rules to underlay switches



Optimal Rule Update

Measurements show that *optimal order* of rule insertion varies with switch platform Goal: Control the actual rule insertion using the

INBOUND

PERFORMANCE

 Prototyped on a commodity host(Intel quad core, 2.66Ghz, 8GB RAM)

OUTBOUND

• Simulated failover scenario in a tunneled WAN

pattern that is optimal for the switch



Goal: Physically decouple the switch's handling of packet_in and packet_out messages from flow_mod



Proxy almost completely eliminates the inbound delay

Flow Arrival Rate (per sec)	Delay w/o Proxy (msec)	Delay with Proxy (msec)
200	8	0.01
2000	-	0.02

Flow Arrival Rate (per sec)	99th percentile delay w/o Proxy (msec)	99th percentile delay with Proxy (msec)
200	192	0.07
2000	-	3.5

Network

- *Topology:* Full mesh with 25 nodes \bullet
- *Traffic matrix:* Assign a popularity index to each node
- Table occupancy: Assume switches have some preinstalled rules
- *Workloads:* 6 workloads which have different table occupancies and traffic volumes

■ base case ■ FE ■ FE + RO

